ABSTRACT

An LCD display comprises a glass substrate, a plurality of first conductive lines, a dielectric layer, and a plurality of second conductive lines. An upper surface of the glass substrate can be divided into a display region and a surrounding frame region. The pixel devices are located at the display region, and each of the pixel devices comprises a thin film transistor (TFT) utilized as a switch. The first conductive lines are located at the frame region to control on and off of part of the TFTs, the dielectric layer is also formed at the frame region for covering the first conductive lines, and the second conductive lines are formed on the dielectric layer to control on and off of the rest of the TFTs.